## Section 10

# **MTL SEMINAR SERIES**

MTL hosts a series of talks each semester known as the MTL Seminar Series. Speakers for the Series are selected on the basis of their knowledge and competence in the areas of microelectronics research, manufacturing, or policy. The MTL Seminar Series is held on the MIT Campus on Tuesdays at 4:00 pm, and is open to the public. A listing of recent seminars is also provided at <a href="http://mtlweb.mit.edu">http://mtlweb.mit.edu</a>. Streaming videos of the MTL Seminar Series are available online exclusively to individuals whose companies are members of the Microsystems Industrial Group (MIG) at MTL. For information regarding the MTL Seminar Series, send e-mail to rmaynard@mtl.mit.edu.

#### September 12, 2006

The Past, Present, and Future of Data Converters and Mixed Signals: A Universal Model

David Robertson, Analog Devices

#### September 26, 2006

Wafer Cleaning in Semiconductor Manufacturing: The Single-Wafer Inflection Point

John TC Lee, Applied Materials

#### November 14, 2006

# "Brain Repair 101" for Electrical Engineers

Timothy Denison, Medtronic Neurological Technologies

### December 5, 2006

Overview of Communication Technology Research at IBM

Mehmet Soyuer, IBM

#### December 12, 2006

Extended Drain CMOS Devices in Sub-100nm Processes Enabling Highperformance Mixed-signal and Direct Battery Connection Analog Blocks

Baher Haroun, Texas Instruments

#### February 27, 2007

# An Introduction to Ultra-High Speed Data Conversion

Gabriele Manganaro, National Semiconductor

#### March 6, 2007

High Mobility III-V MOSFET Technology Matthias Passlack, Freescale Semiconductor

# March 13, 2007 Why Are We So Interested in Carbon

Nanotubes?

Mildred Dresselhaus, MIT

#### April 3, 2007

Unconventional Nanofabrication: Nanosciving, Crack Replication, and Related Techniques

George Whitesides, Harvard University

### April 10, 2007

Trends and Requirements of Future CMOS FETs Based on a Simple Physical Device Model

Dimitri Antoniadis, MIT

### April 24, 2007

Nanoscale CMOS Transistor Scaling Challenges and Strategy for High-Performance Low-Power Logic Technology Tahir Ghani, Intel

May 1, 2007

MEMS at the Knee of the Curve Roger Howe, Stanford University

May 15, 2007

MTL Doctoral Dissertation Seminar Lane Brooks, MIT

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