

VLSI SEMINAR SERIES

MTL hosts a series of talks each semester known as the MTL VLSI Seminar Series. Speakers for the Series are selected on the basis of their knowledge and competence in the areas of microelectronics research, manufacturing, or policy. The MTL VLSI Seminar Series is held on the MIT Campus on Tuesdays at 4:00 pm, and is open to the public. A listing of recent seminars is also provided at <http://mtlweb.mit.edu>. Streaming videos of the VLSI Seminar Series are available online exclusively to individuals whose companies are members of the Microsystems Industrial Group (MIG) at MTL. For information regarding the MTL VLSI Seminar Series, send e-mail to rmaynard@mtl.mit.edu.

fall 2005

Limits on ADC Power Dissipation
September 20, 2005 | Boris Murmann, Stanford University

High Performance Link Challenges and Tradeoffs Above 5Gb/s
September 27, 2005 | Jared Zerbe, Rambus

Adaptive Designs for Power and Thermal Optimization
October 4, 2005 | Sam Naffziger, Intel Corporation

Analog Design: Art or Science?
October 25, 2005 | Mar del Mar Hershenson, SabioLabs

Silicon Process Directions in the Nanoelectronics Era
November 1, 2005 | Mark Pinto, Applied Materials

0-60GHz in Three Years: mm-Wave CMOS Research at BWRC
November 8, 2005 | Ali Niknejad, UC Berkeley

All-Digital RF Frequency Synthesizer and Transmitter for Cellular Phones
November 29, 2005 | Robert Bogdan Staszewski, Texas Instruments

Measurement Tools for Electronics and the Life Sciences
December 6, 2005 | Jim Hollenhorst, Agilent Technologies

Bioelectronics
February 14, 2006 | Rahul Sarpeshkar, MIT

Transformation of CMOS Scaling: How Many "Moore" Generations?
February 28, 2006 | Ghavam G. Shahidi, IBM

Photovoltaics and the Epoch of Renewable Energy
March 7, 2006 | Emanuel (Ely) Sachs, MIT

Microchips for Implantable Drug Delivery and Biosensing
March 14, 2006 | John Santini, MicroCHIPS, Inc.

Integrative MEMS Materials and Processes for Bio-Micro-Systems
April 25, 2006 | Reza Ghodssi, University of Maryland

FPGA Ride the Tide of Moore's Law
May 16, 2006 | Ivo Bolsens, Xilinx Inc.

spring 2006