
Foreword

This annual report contains the research and educational activities of the faculty, staff, and students of the Microsystems Technology Laboratories (MTL). It spans research from circuits and systems to microfabrication. As in previous years, we see continued growth in the size and scope of programs associated with MTL. We have also continued to make significant advances in improving the physical infrastructure of the laboratories. The 6" conversion is proceeding, and we expect to be fully converted in this calendar year. Additionally, we have added the new Exploratory Materials Laboratory (EML), which will serve as a highly flexible, thin-film microfabrication resource. Finally, we are pleased to welcome Prof. Judy Hoyt in the faculty ranks of the EECS department and MTL. Judy will reside in MTL, and her research portfolio will greatly strengthen the MTL and our core silicon process and device research.

During the past year, we have been conducting a strategic planning exercise to help define the future vision for MTL. The original vision for MTL has stood us well for the past 15 years, and we are taking the time to investigate how we might adapt this vision with the times. We will conclude this planning process in a few months, and look forward to communicating our vision for the future.

I'd like to conclude by expressing my sincere appreciation to the administrative, computation, and fabrication staff of MTL. Your dedication and hard work help to make a microsystems community of a collection of faculty and students. MTL enjoys an international stature which you helped create, and which unfold from these pages.

Thank you.

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